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By Chase O'Donnell On 1/31/07
Name _____ Date _____

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,135,767

Atty Docket: 10020699-1

Issued: November 14, 2006

Inventor(s): Wong et al.

Title: INTEGRATED CIRCUIT SUBSTRATE MATERIAL AND METHOD

**REQUEST FOR ISSUANCE OF
CERTIFICATE OF CORRECTION**

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

Sir:

Agilent Technologies, Inc., assignee of the entire interest in the above-identified patent, respectfully requests that a Certificate of Correction be issued in accordance with 37 C.F.R. 1.322 and/or 1.323.

Enclosed herewith is a completed copy of Form PTO/SB/44 describing the various errors involved in the above-identified patent.

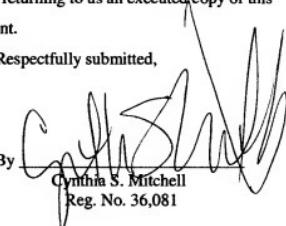
1. The errors are a result of USPTO mistake. NO FEE is required.
2. The errors are a result of Applicant's mistake. \$100 Fee required.
3. The errors are a result of both USPTO and Applicant's mistakes. \$100 Fee required.

If boxes 2 or 3 above are checked, please charge the \$100.00 fee as set forth in 1.20(a), for Applicant's errors (37 C.F.R. 1.323), to our Deposit Account 50-1078. A duplicate copy of this letter is enclosed.

Issuance of a Certificate of Correction to correct these errors, which occurred in good faith is therefore respectfully requested. We shall appreciate your returning to us an executed copy of this Certificate of Correction for attachment to our Letters Patent.

Respectfully submitted,

By



Cynthia S. Mitchell
Reg. No. 36,081

AGILENT TECHNOLOGIES, INC.
Legal Department, DL 429
Intellectual Property Administration
P.O. Box 7599
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Phone No. (970)679-3238

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 _____ of 1 _____

PATENT NO. : 7,135,767

APPLICATION NO.: 10/629,271

ISSUE DATE : November 14, 2006

INVENTOR(S) : Wong et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 6, line 6, in Claim 7, after "semiconductor" insert -- substrate --.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Agilent Technologies (Intelleivate Proofing)

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file items by the USPTO to process an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and suggestions for reducing such time may be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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